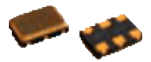




# BSM87L SERIES

## LVDS COMPATIBLE OSCILLATOR

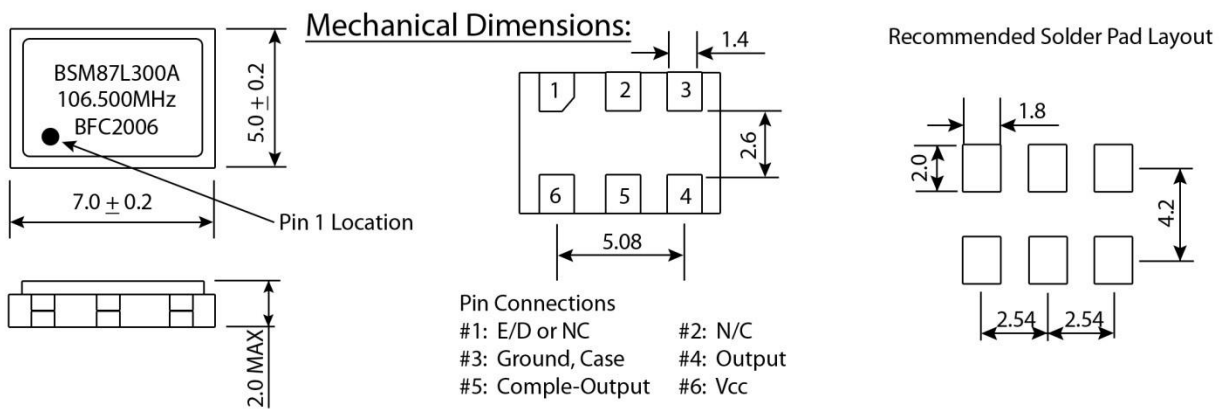
### 7.0 X 5.0 X 2.0MM CERAMIC SMD PKG.



#### Features:

- 80.0 to 320 MHz Frequency Range
- LVDS Compatible Signals
- Inherent Low Power and Low EMI Emission
- Very Low Phase Jitter
- **RoHS Compliant**
- Complementary Output (Output 2)
- 7.0 x 5.0 x 2.0mm SMD Package
- No Internal PLL Avoids Cascading PLL Problems
- Tri-State Enable / Disable
- Tape and Reel Packaging Available

ELECTRICAL SPECIFICATIONS					
Frequency Range (MHz)	80.0 to 320 MHz				
Input Voltage	+3.3 VDC $\pm$ 5%				
	+2.5 VDC $\pm$ 5%				
Overall Frequency Stability	$\pm$ 100ppm; $\pm$ 50 ppm; $\pm$ 25ppm				
Temperature Range	0°C to +70°C; -40°C to +85°C				
Duty Cycle (@ 50% Vp-p)	60/40%; 55/45%				
Output Load	100 Ohms Across Differential Outputs (Offset 1.25V Typical)				
Logic "1" / Logic "0" Level	+1.43V typical / +1.10V typical				
Rise and Fall Time (Tr/Tf)	0.7 ns Max., 0.3 ns Typical, Measured Between 20% to 80% Vp-p				
Start Up Time	5 ms Max.				
Phase Jitter (RMS, 1 Sigma)	1 ps Max. fj. > 1 kHz; 0.3 ps fj = 12 kHz to 20 MHz				
Tristate Function	Input (Pin 1) High (>0.7 Vcc) or Open: (Pin 4,5) Active				
	Input (Pin 1) Low (<0.3 Vcc): Output Disabled in High Impedance				
Enable / Disable Time	200 ns Maximum				
Part Number Table					
Model	Input Voltage	Duty Cycle	Stability	Temp. Range (°C)	Frequency
BSM87L	3 = +3.3 VDC $\pm$ 5%	Blank = 60 / 40%	B = $\pm$ 100ppm	Blank = 0° to 70°C	In MHz
	2 = +2.5 VDC $\pm$ 5%	S = 55 / 45%	C = $\pm$ 50ppm	M = -40° to 85°C	
			E = $\pm$ 25ppm		



All dimensions are typical unless otherwise specified

Dimensions in Millimeters