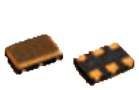
BSM87P SERIES

ECL /PECL SMD OSCILLATOR



Features:

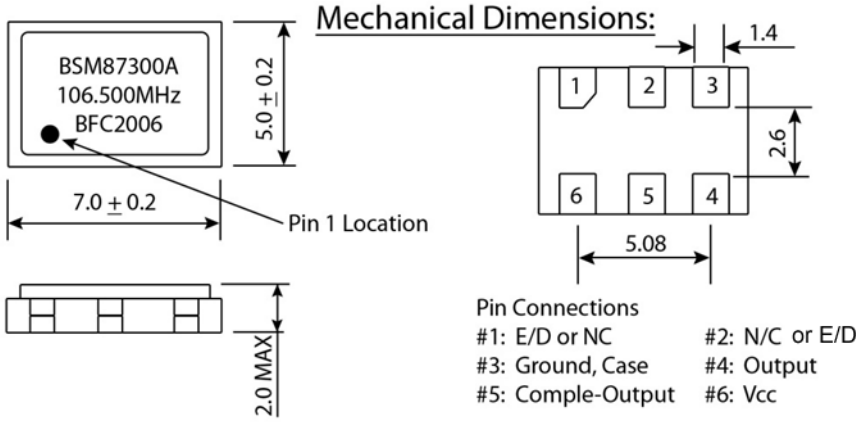
- RoHS Compliant
- Best Suited for ECL / PECL Logic Devices
- Very Low Phase Jitter, Excellent Noise Margin, No Internal PLL
- Leadless Chip Carrier (LCC) Ultra Small Package 7.0x5.0x2.0mm
- Complimentary Output, Tri-State Enable/Disable Standard or Option

ELECTRICAL SPECIFICATIONS	
Frequency Range (MHz)	10.0 to 320 MHz
Input Voltage (Vcc)	+3.3 VDC ± 0.3 VDC
	+2.5 VDC ± 5%
Overall Frequency Stability	± 100ppm; ±50 ppm; ± 25ppm; ± 20ppm
Temperature Range	0°C to +70°C; -40°C to +85°C, -10°~ 70°C
Standard Stability	±50ppm / 0°C to 70°C
Symmetry	60/40%; 55/45%; 52.5/47.5%;
Output Load	50 Ohms to Vcc – 2V Thevenin Equivalent, Bias Required
Logic "1" / Logic "0" Level	(Vcc -1.02V) Min. / (Vcc -1.63) Max
Rise and Fall Time (Tr/Tf)	1 ns Max. Measured Between 20% to 80% Vp-p
Start Up Time	5 ms Max.
Phase Jitter (RMS, 1 Sigma)	1 ps Max. fj > 1 kHz; 0.3 ps fj = 12 kHz to 20 MHz
Tristate Function	Input (Pin 1) High (>0.7 Vcc) or Open: Output (Pin 4,5) Active
	Input (Pin 1) Low (<0.3 Vcc) or Open: Output Disabled in High Impedance
Enable / Disable Time	100 ns Maximum
Pin 1 / Pin 2 Options	No Connection Pin #1, Enable/disable Pin#2 (Suffix "A")
	Enable Disable Pin#1, No Connection Pin#2 (Suffix"B")

Part Number Table

Model	Input Voltage	Stability	Symmetry(Tristate)	Temp. Range (°C)	Pin1/2 options	Freq.
BSM87P	3=+3.3 VDC ±5%	B = ± 100ppm	S = 55/45	Blank = 0° ~70°C	A=N/C #1/ED#2	In MHz
	2=+2.5 VDC ±5%	C = ± 50ppm	Blank = 60/40	M = -40°~ 85°C	B=E/D#1/N/C#2	
		E = ± 25ppm	ST = 52.5 / 47.5	D =-10°~ 70°C		
		F= ± 20ppm				

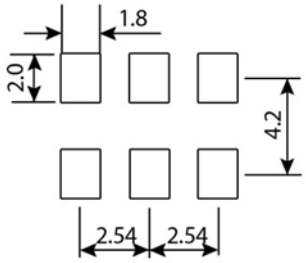
Mechanical Dimensions:



Pin Connections

#1: E/D or NC
#2: N/C or E/D
#3: Ground, Case
#4: Output
#5: Comple-Output
#6: Vcc

Recommended Solder Pad Layout



All dimensions are typical unless otherwise specified

Dimensions in Millimeters