



B1149 OSCILLATOR SERIES

ECL / PECL / LVPECL OUTPUT, DIL-14 PACKAGE



- DIL-14 Thru Hole Package
- Designed to Drive Standard ECL 10K and 10KH Families
- 10 to 200MHz(ECL), .75 to 800MHz (PECL) Freq. Range
- Internal 470W Output Pull-Down Resistor Available
- Very Low Phase Jitter,
- ECL, PECL and LVPECL Options Available
- Dual Complementary Output Available
- 50 Ohm Drive Capability
- Excellent Noise Margin
- **RoHS Compliant** Hermetically Sealed Package

ELECTRICAL SPECIFICATIONS		
Model		B1149
Frequency Range (MHz)		10 to 200 MHz (ECL), 750kHz to 800 MHz (PECL)
Input Voltage	PECL	Vcc = +5.0 VDC ± 5%; for B1149BA & B1149CA
	LVPECL	Vcc = +3.3 VDC ± 5%; for B1149BAL & B1149CAL
	ECL	Vee = -5.2 VDC ± 5%; for B1149AC & B1149BC
Input Current		60 mA Maximum
Storage Temperature		-55°C to 125°C
Overall Frequency Stability		±100ppm; ±50ppm; ±25ppm
Temperature Range (°C)		0°C to +70°C; -40°C to +85°C; -10°C to +70°C
Duty Cycle (at 50% Vp-p)		No Tristate 60/40%; No Tristate 55/45%
Output Load		50 Ohms
ECL	Logic "0" Level	-1.0V MIN
	Logic "1" Level	-1.6V MAXIMUM
PECL	Logic "0" Level	+4.0V MINIMUM
	Logic "1" Level	+3.42V MAXIMUM
LVPECL	Logic "0" Level	+2.59V MINIMUM
	Logic "1" Level	+1.55V MAXIMUM
Rise / Fall Time (Tr/Tf)		2 ns Maximum, measured between 20% to 80% Vp-p
Start-up time		10 ms Maximum
Phase Jitter (RMS, 1 Sigma)		1 ps Max for fj > 1kHz; 0.3 ps Typical for fj = 12KHz to 20 MHz

Above Specification Standard for Following Pinouts

PIN	B1149AC	B1149BA	B1149BC	B1149BM	B1149CA
1	N/C	Comp Out	Comp Out	Comp Out	N/C
7	Vee	Vss & Case GRN	Vee	Vss & Case GRN	Vss & Case GRN
8	ECL Out	PECL Out	ECL Out	ECL Out	PECL Out
14	Vss & Case GRN	Vcc	Vss & Case GRN	Vee	Vcc

Part Number Table

Model	Pinout	Stability	Temp. Range	Symmetry	Resistor	Freq.
B1149	CA	Blank = 100ppm	Blank = 0°C to 70°C	Blank = 60/40%	Blank = None	
	BA	50 = 50ppm	H = -10°C to +70°C	S = 55/45	R = Resistor	
	AC	25 = 25ppm	M = -40°C to +85°C			
	BC					
	BM					

