



# BVT1653T SERIES

## HCMOS/TTL SMD VCTCXO



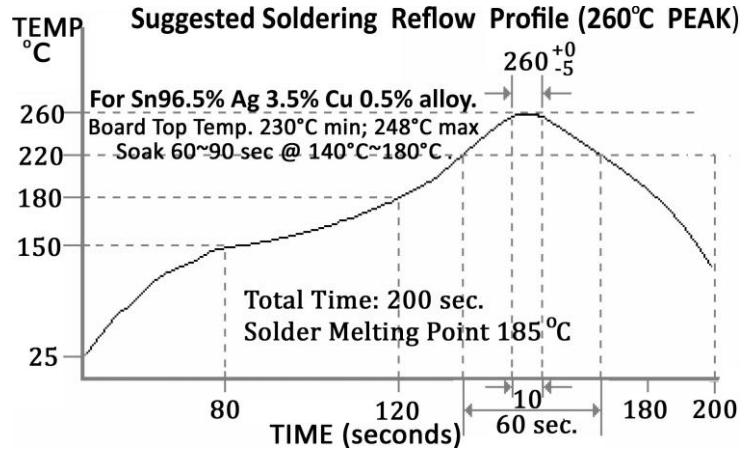
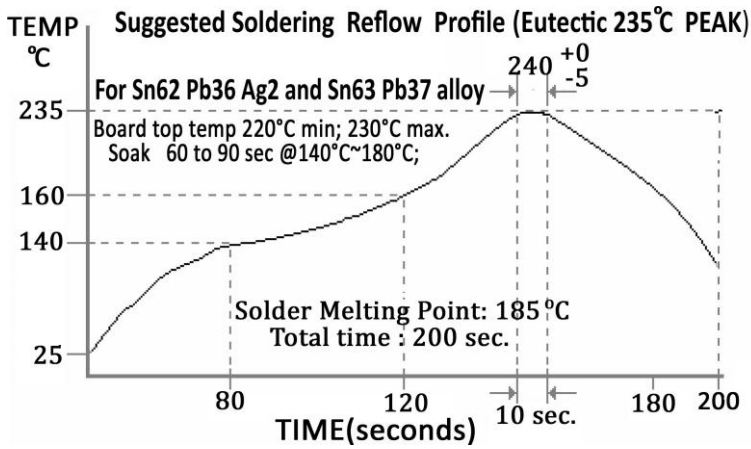
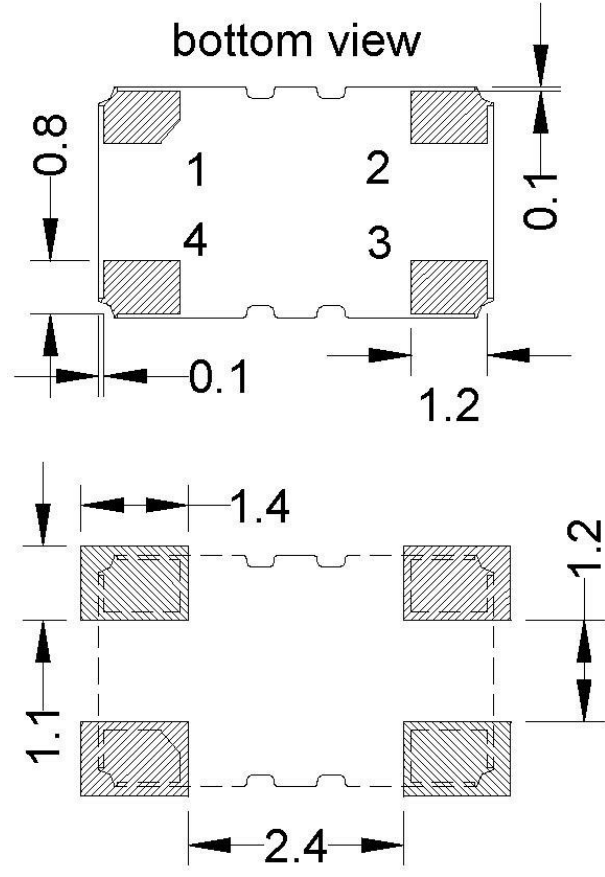
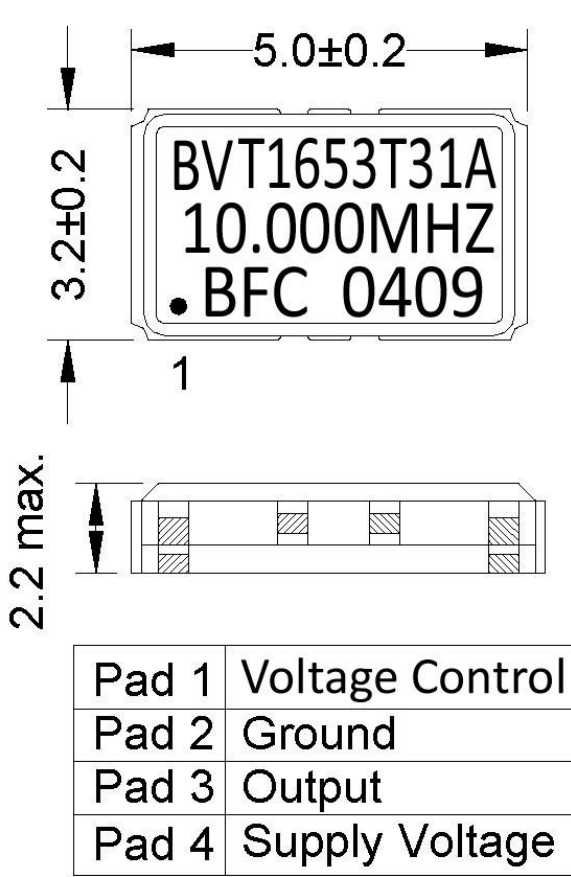
- 5.0 x 3.2 x 2.2 mm Ceramic SMD VCTCXOs
- HCMOS SQUARE WAVE OUTPUT
- NO MECHANICAL TRIMMER FOR AQUEOUS WASHING
- RoHS Compliant
- 0.01 uF DECOUPLING CAPACITOR BUILT-IN
- WIDE FREQUENCY RANGE: 1.0 MHZ TO 200.0 MHZ
- FREQUENCY STABILITY AS TIGHT AS  $\pm 0.5$  PPM AVAILABLE
- -40 TO +85°C TEMPERATURE RANGE AVAILABLE

### BVT1653T VCTCXO SERIES SPECIFICATION SHEET

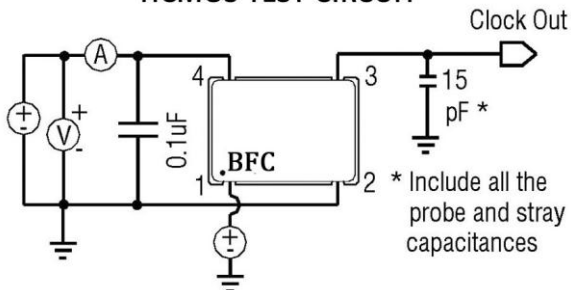
Frequency Range		1.0 MHz to 200.0 MHz				
Output Wave form		Square wave HCMOS				
Initial Calibration Tolerance <sup>(1)</sup>		$\pm 2$ ppm at +25°C $\pm 2$ °C and Vcon = +1.5VDC				
FREQUENCY STABILITY		$\pm 0.5$ ppm	$\pm 1.0$ ppm	$\pm 1.5$ ppm	$\pm 2.0$ ppm	$\pm 2.5$ ppm
TEMPERATURE RANGE	0°C to +50°C	AVAILABLE	AVAILABLE	AVAILABLE	AVAILABLE	AVAILABLE
	-10°C to +60°C	CALL US	AVAILABLE	AVAILABLE	AVAILABLE	AVAILABLE
	-20°C to +70°C	Not Available	AVAILABLE	AVAILABLE	AVAILABLE	AVAILABLE
	-30°C to +75°C	Not Available	AVAILABLE	AVAILABLE	AVAILABLE	AVAILABLE
	-40°C to +85°C	Not Available	AVAILABLE	AVAILABLE	AVAILABLE	AVAILABLE
FREQUENCY STABILITY	Vs. Aging	$\pm 1.0$ ppm max. first year at +25°C				
	Vs. Voltage Change	$\pm 0.3$ ppm max. for a $\pm 5\%$ input voltage change				
	Vs. Load Change	$\pm 0.3$ ppm max. for a $\pm 10\%$ loading condition change				
	Vs. Reflow	$\pm 1$ ppm max. 1 reflow and measured 24 hours afterwards				
Supply Voltage(V <sub>DD</sub> )		+2.8VDC	+3.0VDC	+3.3VDC		
Current Consumption (typical)		2mA @ 8.192MHZ 4 mA @ 10 MHz 17 mA @77.760 MHz 35 mA @ 155.520 MHz				
Output Voltage Range	Logic High "1"	90% (V <sub>DD</sub> ) min.				
	Logic Low "0"	10% (V <sub>DD</sub> ) max.				
Electronic Frequency Tuning Vcon (Pad #1)	Frequency Deviation Range	$\pm 10$ ppm typical with Vcontrol centered at = +1.5 V and Vcontrol range of $\pm 1.0$ V				
	Slope Polarity	Positive: Positive voltage for positive frequency shift				
	Linearity	10 % max.				
Duty Cycle		50% $\pm 10\%$ measured @ 50% V <sub>DD</sub>				
Rise Time and Fall Time		10 ns max. 20% $\leftrightarrow$ 80% of waveform				
Start-Up Time.		10ms max.				
Output Load		15 pF				
SSB Phase Noise @ 25°C	Offset	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz
	3.3V-100.0MHZ	-72dBc/Hz	-110dBc/Hz	-125dBc/Hz	-132dBc/Hz	-125dB/Hz
MSL Level		MSL 1 per IPC/JEDEC-STD-020C				
Humidity		85% RH, 85°C, 48 Hours *Crystal component only*				
Hermeticity		Leak rate $2 \times 10^{-8}$ ATM-cm <sup>3</sup> /sec max				
Solderability		MIL-STD-202F method 208E				
Vibration		MIL-STD-202F method 204, 35G, 50 to 2000Hz				
Shock		MIL-STD-202F method 213B, test conditions E, 1000GG 1/2sine wave				
Storage temperature range		-55 to +125°C				
<b>PART NUMBER GUIDE</b>						
Model	Voltage	Stability	Operating Temperature (°C)		Frequency	
BVT1653T	2 = 2.8V	5 = $\pm 0.5$ ppm	A = 0°C to 50°C			
	3 = 3.0V	1 = $\pm 1$ ppm	B = -10°C to 60°C			
	33 = 3.3V	15 = $\pm 1.5$ ppm	C = -20°C to 70°C			
		2 = $\pm 2$ ppm	D = -30°C to 75°C			
		25 = $\pm 2.5$ ppm	M = -40°C to 85°C			
<b>EXAMPLE</b>						
BVT1653T	3	1	A		50.000 MHz	



# BVT1653T SERIES VCTCXO



**HCMOS TEST CIRCUIT**



**HCMOS OUTPUT WAVEFORM**

