



# BMV5 SERIES VCXO



HCMOS/TTL 7X5X 2.0MM 6 PAD CERAMIC SMD PKG

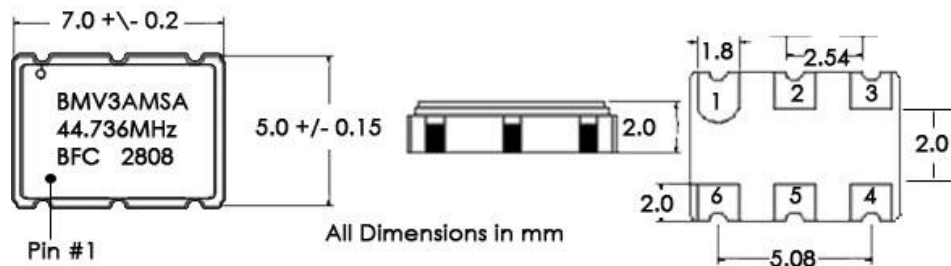
Features:

- HCMOS/TTL Squarewave Output Waveform
- 1.0MHz to 77.760 MHz Frequency Range
- 2.5V Control Voltage (Vcon)
- ± 25ppm Stability Standard (± 50ppm Available)
- -40°C to +85°C Operating Temperature Option Available
- Ground Shielded 6-Pad SMD 5.0x7.0x2.0mm LCC Pkg
- RoHS Compliant (Pb-Free)
- Very Low Phase Jitter with Fundamental Crystal Design
- Leadless Chip Carrier (LCC) Ultra Small SMD Package
- Enable/Disable Control on Pin#2 or Pin#5
- Wide Absolute Pulling Range( To± 100ppm)
- Industry Standard Padout and Footprint

ELECTRICAL SPECIFICATIONS				
Model	BMV5			
Frequency Range (MHz)	1 MHz to 77.760 MHz			
Input Voltage (Vcc)	5.0 VDC ± 5%			
Input Current	30 mA Maximum, depending on Frequency and output load			
Control Voltage (Vc)	+2.5V ± 2.0V			
Storage Temperature	-55°C to 125°C			
Frequency Stability	±25ppm (STD)	±50ppm (Suffix "A")	±50 ppm (Suffix "B")	±25ppm (Suffix "C")
Absolute Pull Range (Min.)	±50ppm (STD)	±50ppm (Suffix "A")	±100 ppm (Suffix "B")	±75 ppm (Suffix "C")
Temperature Range	0°C to 70°C (STD)	-10°C to 70°C (Suffix "G")	-40 to +85°C (Suffix "M")	
Standard Stability / Pullability	BA = ±25 ppm / 0°C to 70°C, Absolute pull range (APR): ±50 ppm Minimum			
Duty Cycle	Tristate 60/40% symmetry(Standard); Tristate 55/45% symmetry(suffix"S")			
Output Load	HCMOS: drive up to 15 pF load; TTL: drive up to 10 TTL gates			
Logic	Logic "1" Level	0.9Vcc Minimum		
	Logic "0" Level	0.1Vcc Maximum		
Rise / Fall Time (Tr/Tf)	5 ns Maximum at 20% to 80% Vp-p			
Start-up time	10 ms Maximum			
Phase Jitter (RMS, 1 Sigma)	1 ps Maximum for fj > 1 kHz; 0.3 ps Typical for fj = 12KHz to 20MHz			
Modulation Bandwidth	12 kHz Minimum at -3dB			
Linearity / Slope	±10% Maximum of best straight line fit / Positive			
Input Impedance	10 k Ohms Minimum			
Setability at Fnom, 25°C	+2.5V ±0.5V			
Tristate Function	Input (Pin 2 or 5) High (>2.2V) or open: Output (Pin 4) active			
	Input (Pin 2 or 5) Low (<0.5V): Output disabled in high impedance			
Enable/Disable Time	100 ns Maximum			

Part Numbering Guide

Model	Stability / (APR)	Operating Temp. Range	Tri-State Symmetry	Pin Connections	Frequency
BMV5	Blank = ±25 / ±50ppm	Blank = 0°C to 70°C	Blank = Tri-State 60/40%	A = E/D Pin#2, N/C Pin#5	
	A = ±50 / ±50 ppm	G = -10°C to 70°C	S = Tri-State 55/45%	B = E/D Pin#5, N/C Pin#2	
	B = ±50 / 100 ppm	M = -40 to +85°C			
	C = ±25 / 75 ppm				
Example					
BMV5	C	M	S	A	44.736 MHz



Pin Connections

- #1 : Voltage Control
- #2 : E/D(A) or N/C(B)
- #3 : Ground
- #4 : Output
- #5 : N/C(A) or E/D(B)
- #6 : Vcc